



BYD Microelectronics Co., Ltd.

BG1000B12LNS

IGBT Power Module

$V_{CE}=1200V$ $I_C=600A$

General Description

BYD IGBT Power Module BG1000B12LNS provides ultra low conduction loss as well as high short circuit capability, which introduce the advanced IGBT chip/FWD and improved connection, it is able to take on a perfect performance in various applications up to 15KHz.

Features

- Half-bridge
- Ultra low loss IGBT technology
- Low inductance case
- Including fast & soft recovery anti-parallel FWD

Applications

- Switching mode power supplies
- UPS
- General power switching applications
- Inverters and DC choppers
- Electronics welders
- Electronics Automobile driver
- AC motor speed control



Characteristic values

Parameter	Symbol	Conditions	Temperature	Value	Unit
Absolute Maximum Ratings					
Collector-emitter voltage	V_{CES}	$V_{GE}=0V$	$T_{vj}=25^{\circ}C$	1200	V
Continuous collector current	I_c	—	$T_c=80^{\circ}C$	600	A
Peak collector current	I_{CRM}	$I_{CRM}=2I_c$	—	1200	A
Gate-emitter voltage	V_{GES}	—	—	+/-20	V
Total power dissipation	P_{tot}	per switch (IGBT)	$T_c = 25^{\circ}C$	1800	W
IGBT short circuit SOA	t_{psc}	$V_{CC}=600V, V_{GE}\leq 15V$ $V_{CEM}\leq 1200V$	$T_{vj}\leq 125^{\circ}C$	10	us
Junction temperature	T_{vj}	—	—	-40~150	$^{\circ}C$
Storage temperature range	T_{stg}	—	—	-40~125	$^{\circ}C$
Diode DC forward current	I_F	—	$T_c=80^{\circ}C$	600	A
Peak forward current	I_{FRM}	$I_{FRM}=2I_F$	—	1200	A
I^2t -value, Diode	I^2t	$V_R=0V, t=10ms$	$T_{vj}=125^{\circ}C$	—	kA^2s
Isolation voltage	V_{isol}	$t=1min, f=50Hz$	—	2500	V



Parameter	Symbol	Conditions	Temperature	Value			Unit	
Characteristics								
IGBT				min.	typ.	max.		
Gate-emitter threshold voltage	$V_{GE(th)}$	$I_C=24mA, V_{GE}=V_{CE}$	$T_{vj}=25^{\circ}C$	5.8	6.4	7.0	V	
Collector-emitter cut-off current	I_{CES}	$V_{CE}=1200V, V_{GE}=0V$	$T_{vj}=25^{\circ}C$	—	—	3	mA	
			$T_{vj}=125^{\circ}C$	—	5	—	mA	
Collector-emitter saturation voltage	$V_{CE(sat)}$	$I_C=600A, V_{GE}=15V$	$T_{vj}=25^{\circ}C$	—	2.1	—	V	
			$T_{vj}=125^{\circ}C$	—	2.4	—	V	
Integrated gate resistor	R_{Gint}	—	$T_{vj}=25^{\circ}C$	—	1.25	—	Ω	
Input capacitance	C_{ies}	$V_{CE}=25V, V_{GE}=0V,$ $f=1MHz$	$T_{vj}=25^{\circ}C$	—	38	—	nF	
Reverse transfer capacitance	C_{res}			—	1.6	—	nF	
Turn-on delay time	$t_{d(on)}$	$V_{CC}=600V, I_C=600A,$ $R_{Gon}=R_{Goff}=3.3\Omega,$ $V_{GE}=\pm 15V,$ $L\sigma=80nH,$ Inductive load	$T_{vj}=25^{\circ}C$	—	825	—	ns	
			$T_{vj}=125^{\circ}C$	—	830	—	ns	
Rise time	t_r		$T_{vj}=25^{\circ}C$	—	200	—	ns	
			$T_{vj}=125^{\circ}C$	—	210	—	ns	
Turn-off delay time	$t_{d(off)}$		$T_{vj}=25^{\circ}C$	—	995	—	ns	
			$T_{vj}=125^{\circ}C$	—	1050	—	ns	
Fall time	t_f		$T_{vj}=25^{\circ}C$	—	132	—	ns	
			$T_{vj}=125^{\circ}C$	—	150	—	ns	
Energy dissipation during turn-on time	E_{on}		$V_{CC}=600V, I_C=600A,$ $R_{Gon}=3.3\Omega,$ $V_{GE}=+15V, L\sigma=80nH,$ inductive load	$T_{vj}=25^{\circ}C$	—	35	—	mJ
				$T_{vj}=125^{\circ}C$	—	42	—	mJ
Energy dissipation during turn-off time	E_{off}	$T_{vj}=25^{\circ}C$		—	77	—	mJ	
		$T_{vj}=125^{\circ}C$		—	92	—	mJ	
Diode				min.	typ.	max.		
Forward voltage	V_F	$I_F=600A$		$T_{vj}=25^{\circ}C$	—	2.4	—	V
				$T_{vj}=125^{\circ}C$	—	2.6	—	V
Peak reverse recovery current	I_{RR}	$I_F=600A, V_R=600V,$ $di_F/dt=3200A/us$		$T_{vj}=125^{\circ}C$	—	300	—	A
Recovered charge	Q_{rr}			$T_{vj}=125^{\circ}C$	—	62	—	μC
Reverse recovery time	t_{rr}			$T_{vj}=125^{\circ}C$	—	514	—	ns
Reverse recovery energy	E_{rec}		$T_{vj}=125^{\circ}C$	—	38	—	mJ	



Parameter	Symbol	Conditions	min.	typ.	max.	Unit	
Thermal-Mechanical Specifications							
IGBT thermal resistance junction to case	$R_{th(j-c)}$	per IGBT	—	—	0.07	K/W	
Diode thermal resistance junction to case	$R_{th(j-c)}$	per diode	—	—	0.09	K/W	
Thermal resistance case to heat-sink	$R_{th(c-s)}$	per module	—	0.04	—	K/W	
Dimensions	L x W x H	Typical , see outline drawing	184.3×68.6×27			mm	
Clearance distance in air	da	according to IEC 60664-1 and EN 50124-1	Term. to base:	—	5.0	—	mm
			Term. to term:	—	11.3	—	
Surface creepage distance	ds	according to IEC 60664-1 and EN 50124-1	Term. to base:	—	7.1	—	mm
			Term. to term:	—	23.2	—	
Mass	m	—	—	550	—	g	
NTC-thermistor characteristic values							
Rated resistance	R_{25}	$T_c=25^{\circ}\text{C}$		5.00		K Ω	
Deviation of R100	$\Delta R/R$	$T_c=100^{\circ}\text{C}$, $R_{100}=493\Omega$	-5		5	%	
Power dissipation	P_{25}	$T_c=25^{\circ}\text{C}$			20.0	mW	
B-value	$B_{25/50}$	$R_2=R_{25}\exp[B_{25/50}(1/T_2-1/(298.15K))]$		3375		K	
B-value	$B_{25/80}$	$R_2=R_{25}\exp[B_{25/80}(1/T_2-1/(298.15K))]$		3411		K	
B-value	$B_{25/100}$	$R_2=R_{25}\exp[B_{25/100}(1/T_2-1/(298.15K))]$		3433		K	

Thermal and mechanical properties according to IEC 60747 – 15

Electrical characteristics according to IEC 60747 – 9

Specification according to the valid application note.

Characterization curves

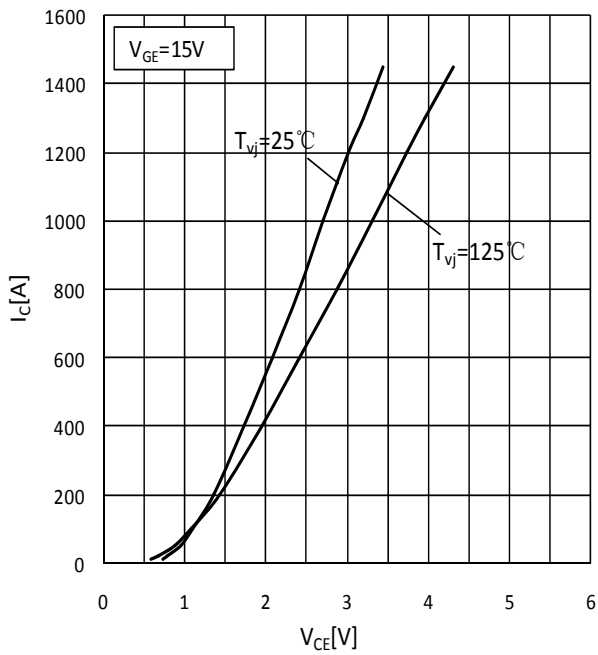


Fig.1 Typ. On-state Characteristics

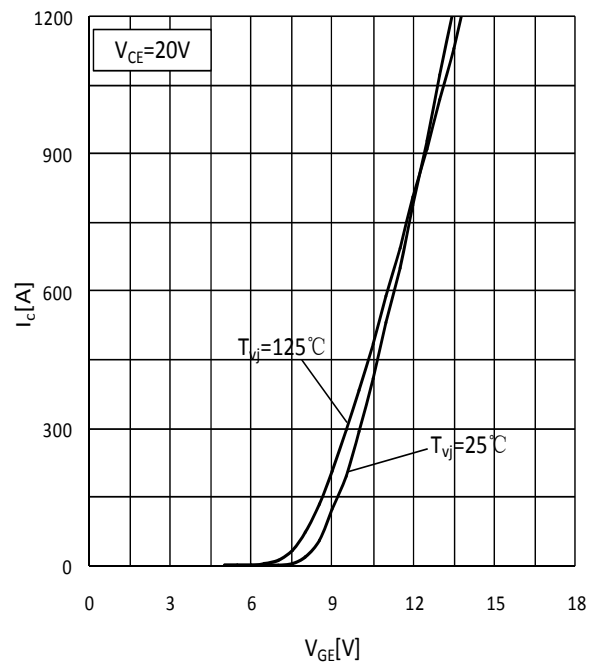


Fig.2 Typ. Transfer Characteristics

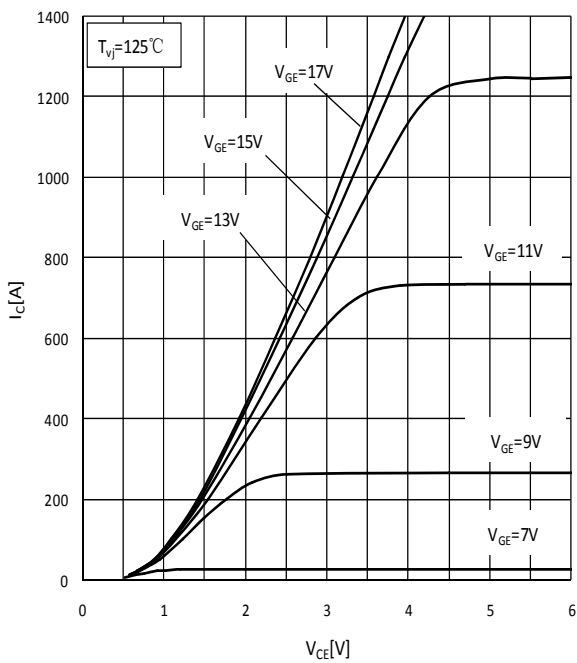


Fig.3 Typ. Output Characteristics

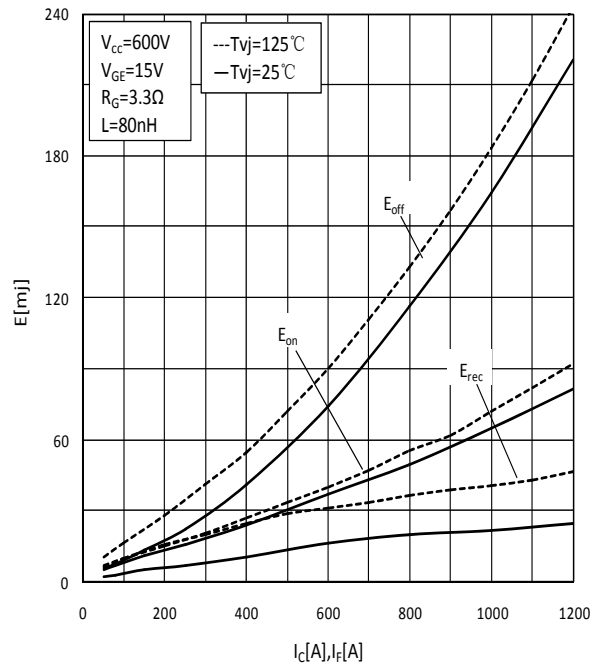


Fig.4 Switching Loss vs. Collector Current

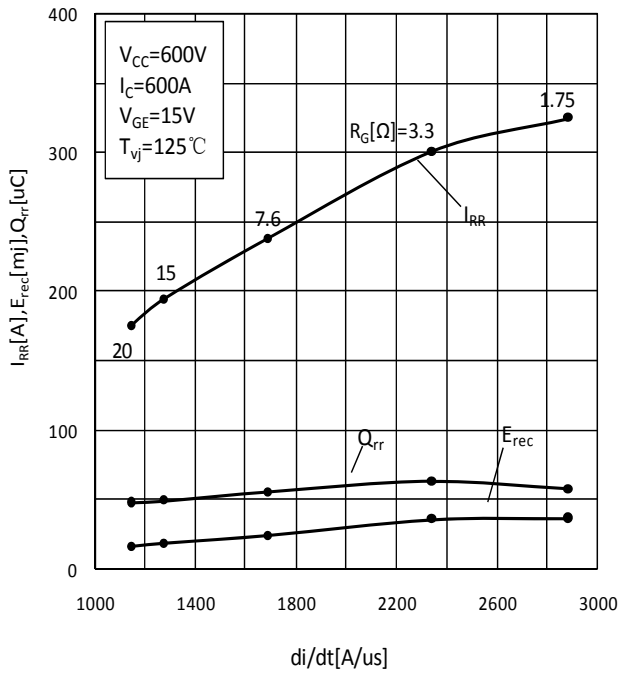


Fig.5 Typ. Reverse Recovery Characteristics

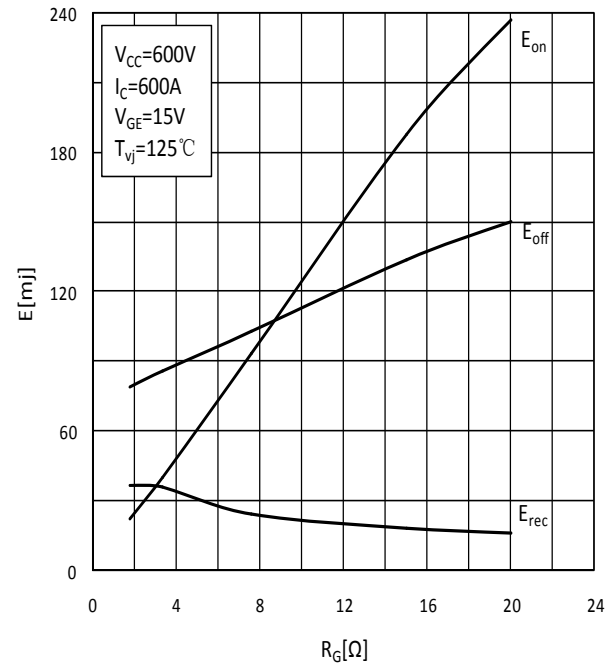


Fig.6 Switching Loss vs. Gate Resistor

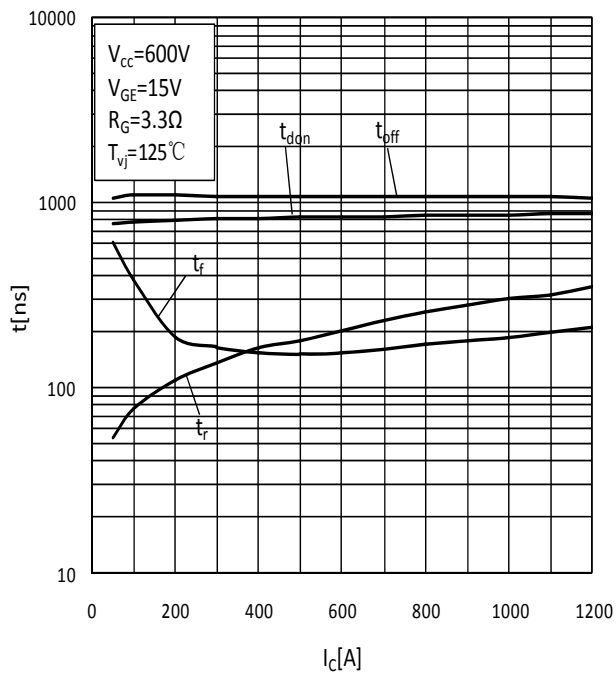


Fig.7 Typ. Switching Times vs. I_c

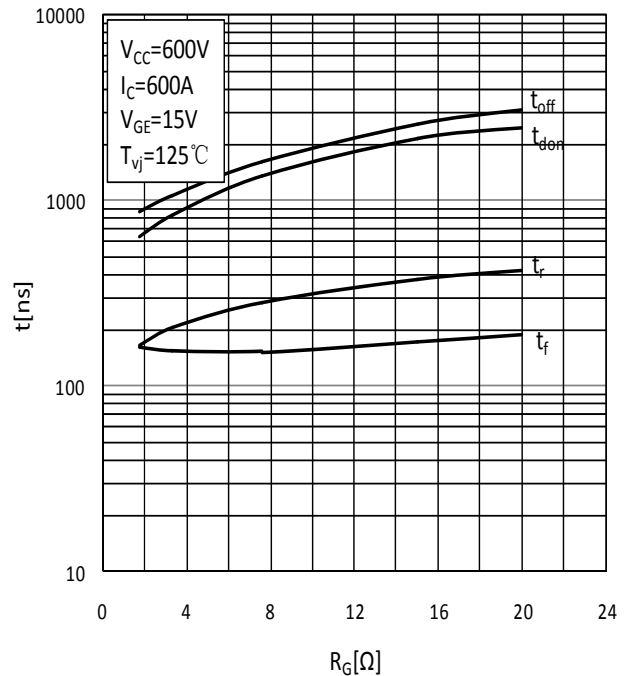


Fig.8 Typ. Switching Times vs. Gate Resistor

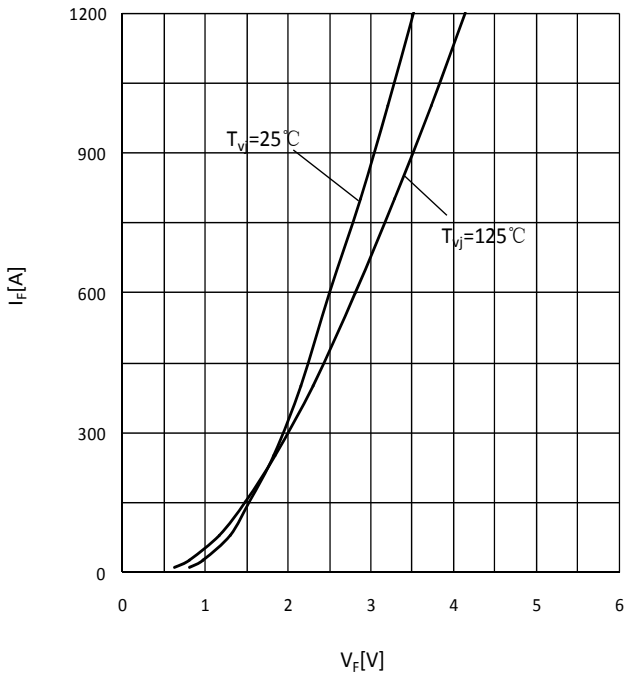


Fig.9 FWD Forward Characteristics.

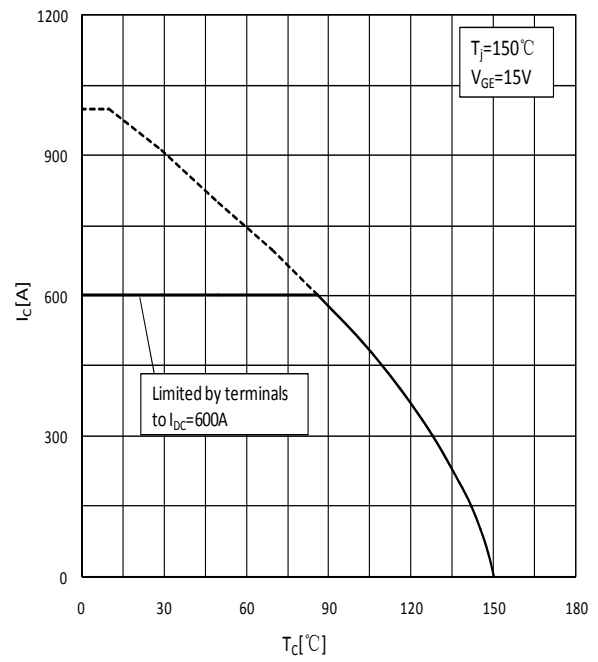


Fig.10 Rate Current vs. Temperature (T_C)

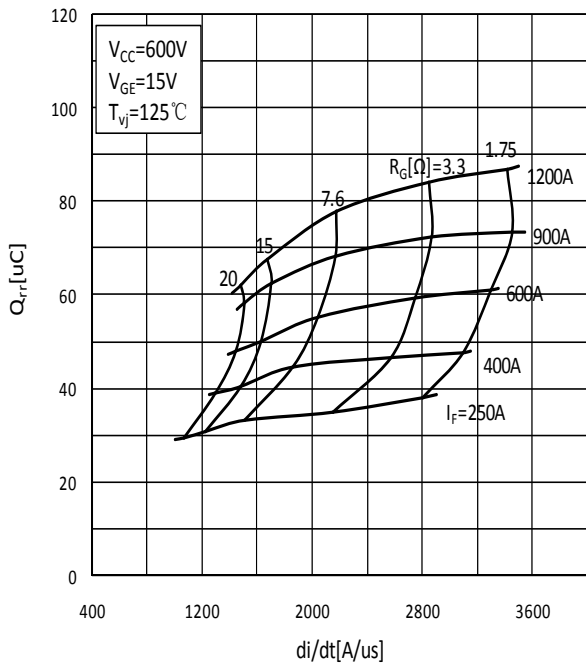


Fig.11 Typ. FRD Recovery Charge

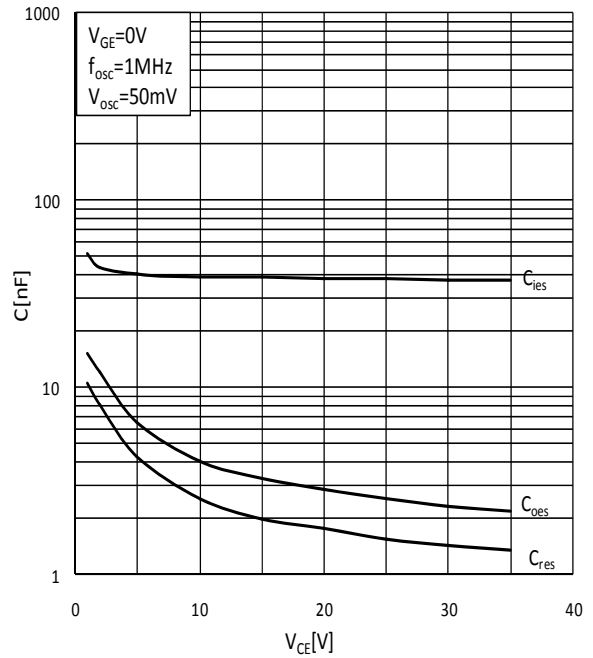


Fig.12 Capacitances vs. Collector-Emitter Voltage

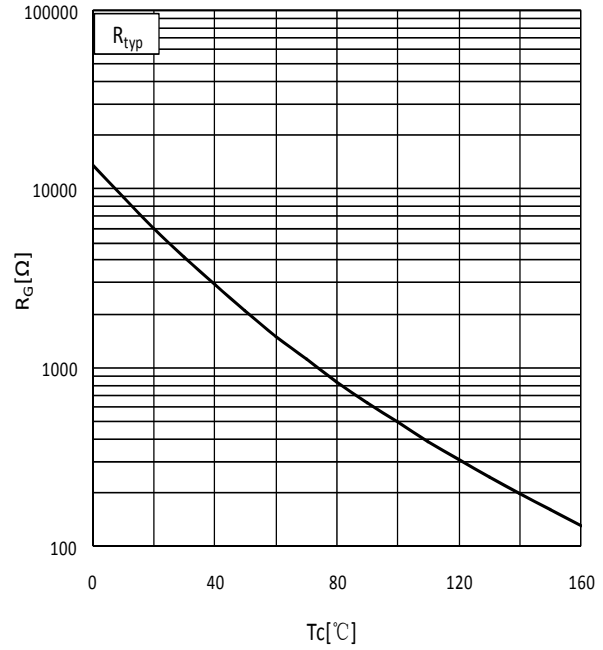
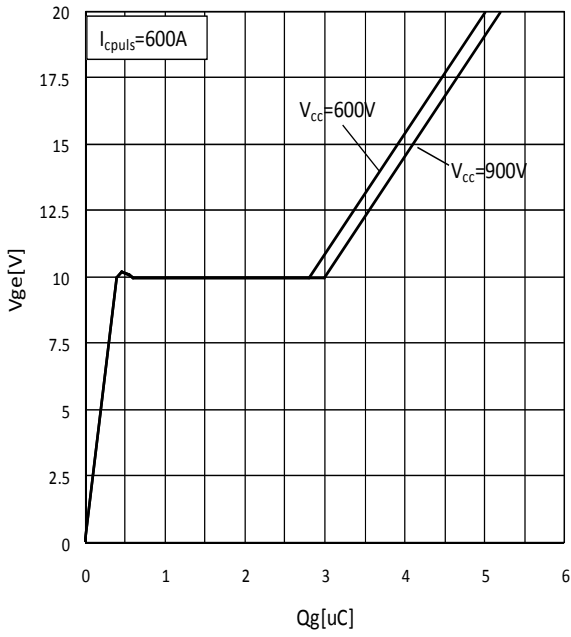


Fig.13 Typ. Gate Charge Characteristics

Fig.14 Typ. NTC-Temperature Characteristics

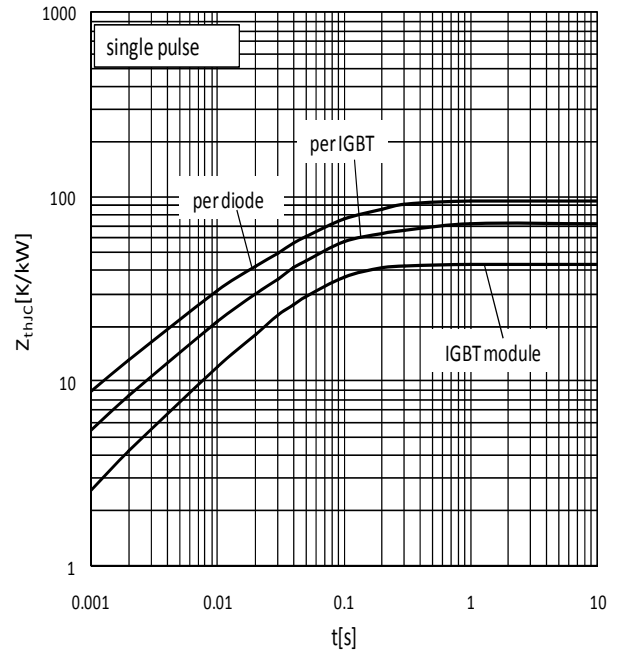
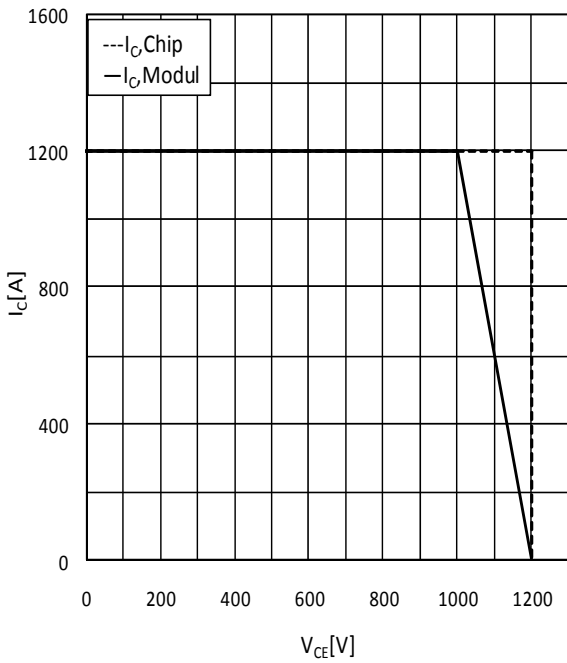
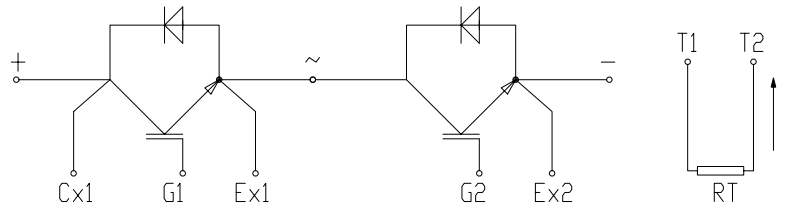


Fig.15 Reverse Bias Safe Operating Area (RBSOA)

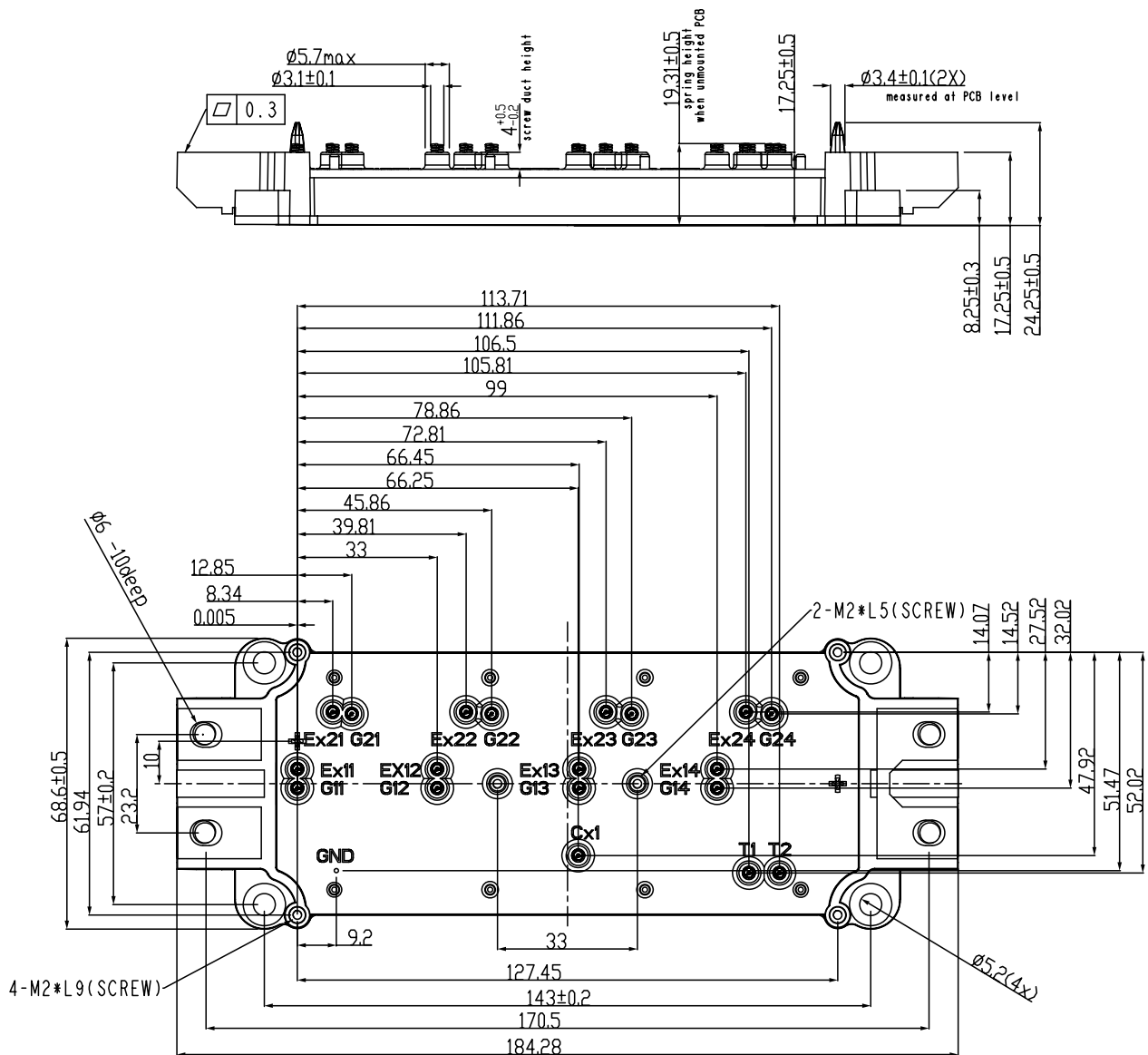
Fig.16 Typ. Transient Thermal Impedance

Circuit Diagram



Package Outlines

Dimensions in mm



Attached (recommended torque):

M2: 0.3~0.4 N • M

M5 : 2~3.5N • M

M6 : 3~4.5N • M



Attention

1. In order to reduce the contact resistance, we suggest add thermal grease between base and heat-sink, which thickness is about 0.1mm.
2. When installing the module, please wear a electrostatic bracelet to prevent the gate breakdown and the imbalance power may damage the internal chip, even to damage the module.
3. This is an electrostatic sensitive device, please observe the international standard IEC 60747-1, chap. IX.

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